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Docket No.: GR 98 P 2340

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : SÖREN HAUBOLD

Filed : Concurrently herewith

Title : METHOD AND CONFIGURATION FOR SUPPLYING A CLOCK  
SIGNAL TO PROCESSOR-CONTROLLED APPARATUSES



INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent No. 5,461,652 (Hongo), dated October 24, 1995;

U.S. Patent No. 5,167,024 (Smith et al.), dated November 24, 1992;

U.S. Patent No. 5,362,998 (Iwamura et al.), dated November 8, 1994;

Published European Patent Application No. 0 332 077 A2 (Iwamura et al.), dated September 13, 1989;

Published European Patent Application No. 0 316 943 A2 (Ito et al.), dated May 24, 1989;

Published European Patent Application No. 0 645 689 A2 (Ichinose et al.), dated March 29, 1995 and

"Dynamic Power Management by Clock Speed Variaton", IBM Technical Disclosure Bulletin, Vol. 32, No. 8B, January 1990.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,



For Applicant WERNER H. STEMER  
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Date: February 14, 2001

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